

Hideki AGARI et al., S.N. 10/594,542  
Page 2

Dkt. 2271/76931

**Amendments to the Specification**

Please amend the paragraphs at page 5, line 13 through page 6, line 16, in the following manner:

**DISCLOSURE OF THE INVENTION SUMMARY**

~~It is a general object of the present invention to provide In an aspect of this disclosure, there is provided~~ a semiconductor apparatus having a protection circuit, which can prevent electric discharge between an electrode of the protection circuit and other semiconductor devices and allow the miniaturization and integration of the apparatus.

~~Features and advantages of the present invention are set forth in the description that follows, and in part will become apparent from the description and the accompanying drawings, or may be learned by practice of the invention according to the teachings provided in the description. Objects as well as other features and advantages of the present invention will be realized and attained by the semiconductor apparatus particularly pointed out in the specification in such full, clear, concise, and exact terms as to enable a person having ordinary skill in the art to practice the invention.~~

~~To achieve these and other advantages and in accordance with the purpose of the invention, as embodied and broadly described herein, the invention provides as follows:~~

**[[There]] In another aspect of this disclosure, there is provided a semiconductor apparatus comprising a substrate with a pad, an internal circuitry region, and a protection resistance formed on the substrate;**

Please amend the paragraph at page 7, lines 7-10, in the following manner:

**[[There]] In another aspect of this disclosure, there is provided a semiconductor apparatus comprising a substrate with a pad, an internal circuitry region, and a protection resistance formed on the substrate;**